




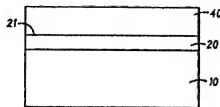


**Method for fabricating a semiconductor structure including a metal oxide interface with silicon****Publication number:** TW468212 (B)**Publication date:** 2001-12-11**Inventor(s):** RAMDANI JAMAL [FR]; DROOPAD RAVINDRANATH [US];  
YU ZHIYI JIMMY [US]**Applicant(s):** MOTOROLA INC [US]**Classification:****- international:** C30B29/16; C30B25/02; H01L21/24; H01L21/316;  
H01L21/8242; H01L21/8246; H01L21/8247; H01L27/105;  
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C30B29/10; C30B25/02; H01L21/02; H01L21/70;  
H01L27/105; H01L27/108; H01L29/66; (IPC-1-7): H01L21/20**- European:** C30B25/02; C30B25/02**Application number:** TW20000120550 20001003**Priority number(s):** US19990425945 19991025**Also published as:** EP1096042 (A1)  
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**Abstract of TW 468212 (B)**

A method of fabricating a semiconductor structure including the steps of providing a silicon substrate (10) having a surface (12), forming on the surface (12) of the silicon substrate (10), by atomic layer deposition (ALD), a seed layer (20; 20') comprising a silicate material and forming, by atomic layer deposition (ALD) one or more layers of a high dielectric constant oxide (40) on the seed layer (20; 20').

**FIG. 9**

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